## REMARKS

Applicants and the undersigned are most grateful for the time and effort accorded the instant application by the Examiner. The Office is respectfully requested to reconsider the rejection present in the outstanding Office Action in light of the following remarks.

The drawings still stand objected to under 37 CFR 1.83(a) for allegedly failing to show every feature of the invention specified in the claims. This objection is respectfully traversed. 37 CFR 1.81(a) states a drawing must be furnished "where necessary for the understanding of the subject matter sought to be patented". In view of the remarks presented below, it is respectfully submitted the drawings are sufficient and this objection should be withdrawn.

Claims 1-4 are pending in the application. Claims 1, 2, and 3 are independent claims; claim 4 is a dependent claim. In the Office Action dated February 20, 2005, the Summary indicates that pending Claim 1 was allowed and Claims 2-4 were rejected; the Office Action itself, however, indicates Claims 1-4 were allowed. (Paragraph 10, page 6) For this response, it is assumed the Summary is correct. If this assumption is not correct, clarification is requested.

Claim 2 stands objected under 35 USC 132, it being asserted "controlling said MUX element in order to be able to read a schema from said FPGA into said EEPROM" is new matter. Claim 2 also stands rejected under 35 USC 112, first paragraph as failing



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to comply with the written description requirement, it being asserted that this same language contains subject matter which was not described in the specification in such a way as to convey to one skilled in the relevant art that the inventors, at the time the application was filed, had possession of the claimed invention. Claims 3-4 also stand rejected under 35 USC 112, first paragraph, it being asserted that the specification does not provide enablement "for said MUX being controllable to permit data to be read from FPGA". This objection and these rejections are respectfully traversed.

With regards to Claim 2, the Examiner has stated that there are four connections available to read configuration data or schema from FPGA into EEPROM without involving control of the MUX element. However, it is respectfully submitted that the specification explicitly states that these four connections are used "**as discussed with reference to the prior art cited above**". Thus, these four connections are not used in the novel methods or method steps presented in the instant application, such as those in Claim 2. Furthermore, both the Abstract and the Summary of the Invention discuss using the limitation of Claim 2 that is in contention. The Summary of the Invention teaches that "**the MUX element can be controlled to select either said PROM device or said EEPROM device or said FPGA device for reading data from said devices**". (Page 5, lines 8-9) The Summary later details a method in which "**the FPGA is used to program the EEPROM with the schema received from a disk**". This method step is detailed in both the specification (Page 5, lines 14-15) and the Abstract (line 15). The method continues to state that "**[T]hen the MUX is switched to be able to read from the EEPROM...**". (Page 5, lines 15-16) Other than the four connections that are used in



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reference to prior art methods, the INIT_CONFIG line is only used when configuring the FPGA with the contents of the EEPROM. (Page 10, lines 4-8) Thus, the communication between the FPGA and the EEPROM as detailed in Claim 2, lines 5-6, must necessarily have occurred through the multiplexer, in which the MUX element must be controlled in order to read data from the FPGA. Thus, the specification provides enablement for the limitation of Claim 2 that was in contention.

With regards to Claim 3, it is respectfully submitted that the specification explicitly states in the Summary of the Invention the limitation in contention, and thus the limitation in contention is enabled. Specifically, the specification states in the Summary of the Invention, "**the MUX element can be controlled to select either said PROM device or said EEPROM device or said FPGA device for reading data from said devices, in order to properly connect said FPGA to said bus system and to initialize a configuration of said FPGA with the contents comprised of said EEPROM**". (Page 5, lines 8-11). Claim 4 was rejected under 35 USC 112 solely because of its dependency on Claim 3. However, as the rejection with regards to Claim 3 has been traversed, it follows that the rejection with regards to Claim 4 has also been traversed.

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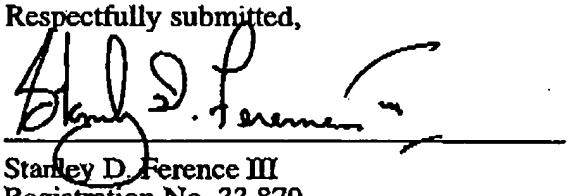


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In summary, it is respectfully submitted that the instant application, including Claims 1-4, is presently in condition for allowance. Notice to the effect is hereby earnestly solicited. The Examiner is invited to telephone the undersigned at the number shown below in the event there are any further issues with this application.

Respectfully submitted,



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